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TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application Number	10/728,216	
	Filing Date	December 4, 2003	
	First Named Inventor	Hou Tao	
	Art Unit	2818	
	Examiner Name	Phuc T. Dang	
Total Number of Pages in This Submission	6	Attorney Docket Number	TS03-352 (N1085-90167)

ENCLOSURES (Check all that apply)		
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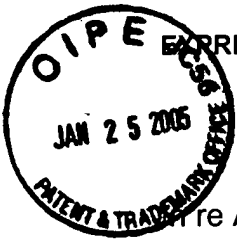
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Firm Name	Duane Morris LLP		
Signature	<i>Steven E. Koffs</i>		
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Re Application of: Hou Tao et al.

Confirmation No. 7980

Serial No.: 10/728,216

Group Art Unit: 2818

Filed: December 4, 2003

Examiner: Phuc T. Dang

For: **METHOD OF FORMING A FLOATING GATE FOR A STACKED GATE FLASH
MEMORY DEVICE**

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RESPONSE TO RESTRICTION REQUIREMENT

Dear Sir:

This is in response to the Restriction Requirement dated January 4, 2005. The Examiner requires an election between Group I, Claims 1-15, and 21-25, drawn to a method to form a floating gate or an electron emitter for a memory device, classified in class 438, subclass 257, or Group II, Claims 16-20 and 26-27, drawn to an apparatus of a flash memory device or a electron emitter device, classified in class 257, subclass 365.

Applicant hereby elects Group 1, Claims 1-15 and 21-25, drawn to a method to form a floating gate or an electron emitter for a memory device, classified in class 438, subclass 257. This election is made without traverse.

Respectfully submitted,

Steven E. Koffs

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Registration No. 37,163

Attorney for Applicant(s)

DATE: 1-25-05

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